

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) WITH (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L2	4	((margin OR extreme) ADJ test\$3) AND computer AND (BMC OR (baseboard AJD management ADJ controller)) AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L3	4	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter) AND (frequency ADJ synthesizer)) AND (communicat\$4 OR transmit\$4 OR receiv\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05

L4	6	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND ((digital ADJ parameter)) AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L5	284	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND (communicat\$4 OR transmit\$4 OR receiv \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L6	24	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv \$3 AND diagnos\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L7	115	((margin OR extreme) ADJ test\$3) AND computer AND frequenc\$3 AND communicat\$4 AND transmit\$4 AND receiv \$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L8	11	((frequenc\$3 ADJ (margin OR extrem \$3)) WITH test\$3) AND server	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L9	56	((frequenc\$3 ADJ (margin OR extrem \$3)) WITH test\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L10	42	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch \$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface) AND operation\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05

L11	1	((integrated ADJ circuit) OR IC) AND amplif\$3 AND (operational ADJ characteristic) AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L12	42	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch \$3 AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05
L13	41	((integrated ADJ circuit) OR IC) AND amplif\$3 AND switch AND (analog ADJ multiplex\$3) AND (serial ADJ communications ADJ interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/13 18:05

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